ESTEVAN CHIA HUNG TU

@ estevan.tu@gmail.com **J** +33 6 98 86 32 76

in linkedin.com/in/estevan-tu-159b37173

• Nationality : Brazilian

EDUCATION

PhD in Electronic Engineering

University of Bordeaux

- 2023 Ongoing
- Bordeaux, France
- CIFRE Thesis with NXP Semiconductors France
- PhD title: "High Quality Factor Oscillator Design for High Performance Radar Waveform Generator"
- M.Sc. Electronic Systems
- **University of Bordeaux**
- 2020 2021

- Bordeaux, France
- Graduating with Highest Honors Second in class (of 37 students)

Masters in Electronic Engineering **ENSEIRB-MATMECA (Bordeaux INP)**

- 2019 2021
- Bordeaux, France
- Graduating with Highest Honors Second in class (of 85 students) • Double degree through the BRAFITEC project of excellence (being
- awarded a full scholarship from the Brazilian government)
- Speciality: Radiofrequency (RF)

Electrical Engineering Federal University of Paraná - UFPR

- 2016 2021
- Double degree at Enseirb-Matmeca through the BRAFITEC project
- Grade: 0.9394/1 (First in class)

EXPERIENCES

Internship NXP SEMICONDUCTORS

- **i** 02/2021 07/2021
- Toulouse, France

Curitiba, Brazil

- Development of new pre-matching structures for GaN transistors to improve their performance for 5G applications
- Design of a stable power amplifier with an output power of 57.2 dBm

Design of a TMTC (2.2 GHz) Tx/Rx radio card for Nanosatellite appli-

cations. Responsible for the Rx reception part

Internship

IMS Laboratory

- 06/2020 09/2020
- Talence, France

Talence, France

SKILLS

Keysight ADS Cadence Virtuoso	
Proteus Radiofrequency RF Design	
RF Measurements	Matlab CST
Arduino C++	Microsoft Office
Communication	Project management

PROJECTS

Présentieirb

- 2020-2021
- Enseirb-Matmeca
- Development of a low-cost electronic device capable of recording student attendance in a contactless manner
- Project created to solve an everyday problem using circuits and systems
- Project presented at the Student Workshop of the International Conference NEWCAS 2021 (IFFF)

Graphogame

- 2020
- Enseirb-Matmeca
- Integration of electronic components with an Arduino to create a device and an interface to obtain information on the force applied by the fingers of a person with dysgraphia
- In association with a graphic therapist

Smart Cat Flap

- 2019
- Enseirb-Matmeca
- Development of a prototype of a smart cat flap allowing the entry of a cat with a passive resonant tag at 13.56 MHz
- Study and development of an oscillator

LANGUES

- PORTUGUESE : bilingual
- SPANISH : bilingual
- ENGLISH : advanced
- FRENCH : advanced
- CHINESE : advanced

